



## Faster TTM with Velocity

Velocity software provides modern, industry-leading automation and processing, enabling unparalleled translation capabilities of WGL, STIL and EVCD simulation vectors to Automatic Test Equipment (ATE) platforms.

Velocity REPLAY creates Verilog test benches encoded with the ATE's exact specifications for "Guaranteed-by-Design" pre-silicon validation and post-silicon ATE test program automation, ensuring first-pass success.

- One Common Interface
- Parallel Conversions
- Customizable & Flexible
- Pre-Silicon Validation with REPLAY
- Burn-In Links
- Same Day Support
- 1 Day Training
- No Perl Scripts Required
- Automated Setup
- Built-In Intelligence

## Key Features

- ATE Rules Checking
- Design File Analysis
- MultiPort/MultiTime Domain
- DataRate/XMODES
- Port Tracking
- Optimized Vector Compression
- Automated Editing Language
- Logical Masking
- Pattern Bursting
- Multi-Simulation Merging (MCM)
- REPLAY to Design with Verilog
- Simulation Resampling
- Vector Size Analysis
- Scan Flop Logging Links
- GUI & Batch Interfaces



# EDA & ATE Specifications

## All Advantest Products

- V93000 EXAScale
  - PS5000, PS1600, MLS, 9G, PS-SL
- SLT: DTS
- SiConic, DV & SV
- T2000
- T5X - Memory

## All Protocols

- JTAG
- I2C, I3C
- SPI
- DWS
- Custom

## All EDA Formats

- STIL
- EVCD/VCD
- WGL
- SVF
- Verilog (REPLAY)

## One-Step Automation



### First-Time-Right Silicon

Pre-silicon verified data works the first time on ATE

### Entire Designs converted

and integrated in hours

### Optimized Vector Memory

Higher fault coverage with existing testers

## Silicon Bring-up in Hours

**Alliance** **ATE**  
CONSULTING GROUP, INC  
**ADVANTEST** Group

**VELOCITYCAE**™  
TESTING AT THE SPEED OF LIGHT